AUG. 1987

MT-32

# MT-32 SERVICE NOTES First Edition

## **SPECIFICATIONS**

**Sound Sources** Preset Tones

: 32 polyphonic : 128

Sampling Frequency : 32KHz Data Format

: 15-bit linear

Noise Level **Current Draw** 

: -80dBm (IHF-A) or less @25°C (77°F) : 650mA @9V

Power Consumption : 10W @100V (AC adaptor input)

9.5W @117V

Dimensions

10.5W @220-240V

: 51 (H) x 305 (W) x 220 (D) mm 2 x 12 x 8-11/16 in

Weight Accessories : 1.53 kg. 3 lb 6 oz : AC Adaptor

ACB-100 100V ACB-120 117V

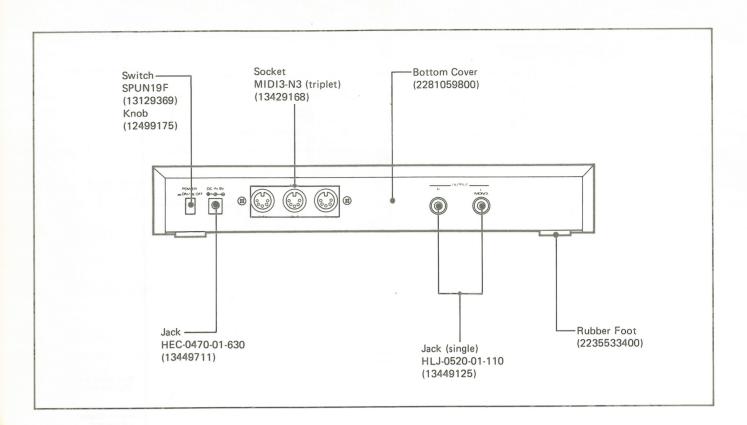
ACB-220 220V ACB-240A 240V (Australia) ACB-240E 240V (England) MIDI Cable (DIN Cord) (1m) 1 pc

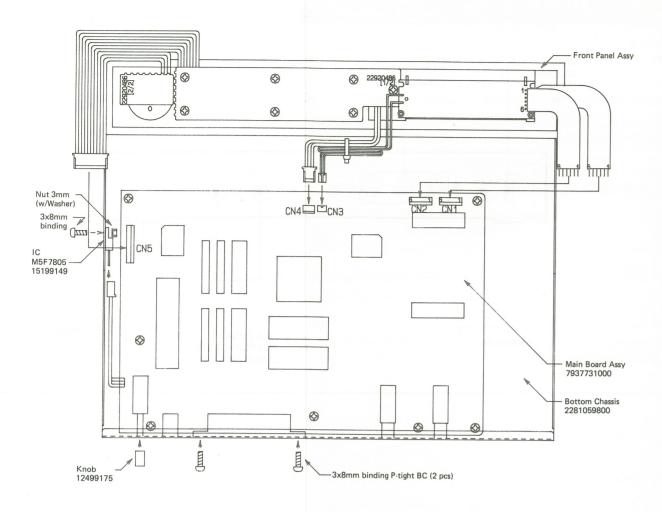
Connection Cord LP-25 2 pcs

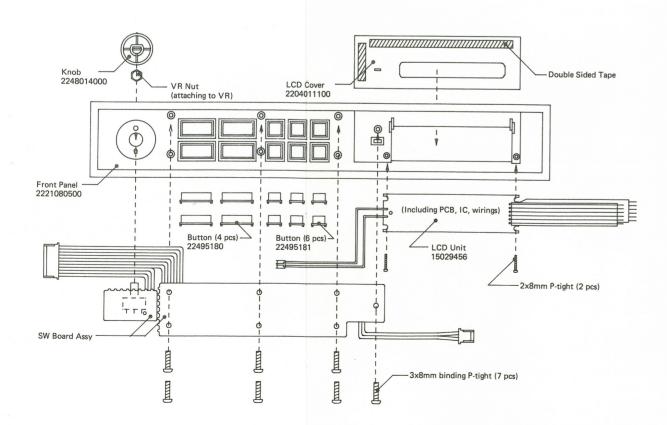
All switches Button (single, 4 pcs) SKELAF (13129736) (22495180) Button (single, 6 pcs) -(22495181) -Top Cover (2202035300) -Front Panel (2221080500) Roland STANSE MT-32 LCD Cover -Knob-(2204011100) (2248014000) LED (green) LCD Unit GL-9PG2 RK12K1140 50KB DM2011 (15029189) (15029456) (13279832)



Printed in Japan BA-2(LH) 1







Viewing at rear of unit

# **EXPLODED VIEW**

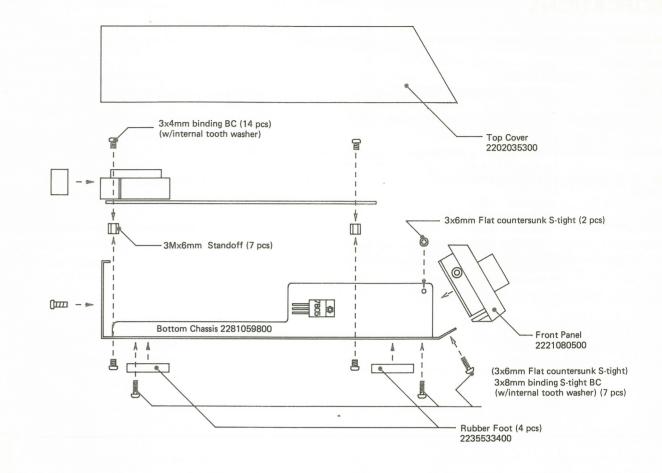
#### NOTE

When inserting or pulling out flat cable at CN1 or CN2, push lock mechanism at the connector.

# 分解図

### 注意

CN1,CN2上でフラットケーブルを抜き差しする場合は、コネクタ上のロックを押して下さい。



# **PARTS LIST**

CASING			
2202035300	Top Cover		
2281059800	Bottom Chassis		
2221080500	Front Panel		
2204011100	LCD Cover		
PCB ASSY			
7937731000	Main Board	(pcb 2292048700)	
7337731000	nain board	(peb 22)2040700)	
7937742000	SW Board	(pcb 2292048600)	
	(including VR b	ooard)	
	VR基板を含む		
LCD UNIT			
15029456	DM2011 (includi	ing PCB, IC, Connect	or and Cable)
		for individual part	
		クタを含む。これらの単独補償	冬部品はありません。
BUTTON, KN			ļ
2248014000	Knob		SELECT/VOLUME
12499175	Knob		POWER
22495181	Button		PART
22495180	Button		SOUND GROUP, VOLUME
			SOUND, MASTER VOL
SWITCH			
13129369	SPUN19F		POWER
13129369	SKELAF		PART, SOUND GROUP
13147/30	DIVERBL		SOUND, M. VOL
			SOUND, FI. VOL
JACK			
13449125	HLT-0520-01-110		OUTPUT
			00=10=
13449711	HEC-0470-01-630		DC IN
13449711 13429168	HEC-0470-01-630 MIDI3-NS (tripl	)	
		)	DC IN
13429168		)	DC IN
13429168 IC	MIDI3-NS (tripl	let)	DC IN
13429168 IC 15179246	MIDI3-NS (tripl	CPU	DC IN
13429168 IC 15179246 15229851	MIDI3-NS (tripl C8095-90 MB87136A	CPU LA chip	DC IN
13429168 IC 15179246 15229851 15229865	MIDI3-NS (tripl C8095-90 MB87136A HG61H15B59F	CPU LA chip gate array	DC IN
13429168 IC 15179246 15229851 15229865 15229863	MIDI3-NS (tripl C8095-90 MB87136A HG61H15B59F HG61H20R36F	CPU LA chip gate array reverb chip	DC IN
13429168 IC 15179246 15229851 15229865 15229863 15219178	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP	CPU LA chip gate array reverb chip D/A converter	DC IN MIDI
13429168 IC 15179246 15229851 15229865 15229863 15219178 15179844	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471	CPU LA chip gate array reverb chip D/A converter 2M mask ROM	DC IN MIDI
13429168 IC 15179246 15229851 15229865 15229863 15219178 15179844 15179845	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM	C IN MIDI  (WAVE) IC21 (WAVE) IC22
13429168 IC 15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM	(WAVE) IC21 (WAVE) IC22 IC27
13429168 IC 15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM	(WAVE) IC21 (WAVE) IC22 IC27 IC26
13429168 IC 15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM	(WAVE) IC21 (WAVE) IC22 IC27
13429168 IC 15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM	(WAVE) IC21 (WAVE) IC22 IC27 IC26
13429168 IC 15179246 15229851 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM	(WAVE) IC21 (WAVE) IC22 IC27 IC26
13429168 IC 15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382 15169515	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND	(WAVE) IC21 (WAVE) IC22 IC27 IC26
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382 15169516	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND quad 2-input NOR	(WAVE) IC21 (WAVE) IC22 IC27 IC26
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382 15169515 15169516	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC04P	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382 15169515 15169516 15169514 15169537	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P TC74HC04P TC74HC27P	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter triple 3-input NOR	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382 15169515 15169516 15169514 15169537 15169334H0	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P TC74HC04P TC74HC04P TC74HC27P HD74LS05P	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM EP ROM O RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter triple 3-input NOR	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382 15169515 15169516 15169514 15169537 15169334H0 15159113H0	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P TC74HC04P TC74HC04P TC74HC27P HD74LS05P HD14051	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM EP ROM O RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter triple 3-input NOR hex inverter with MUX/DEMUX	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15169515 15169516 15169514 15169537 15169537 15169334H0 15159113H0 15199159	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P TC74HC02P TC74HC04P TC74HC27P HD74LS05P HD14051 IR3M03A	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter triple 3-input NOR hex inverter with MUX/DEMUX DC-DC converter	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15169516 15169516 15169514 15169537 15169334H0 15159113H0 15199159 15229706S0	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P TC74HC04P TC74HC04P TC74HC27P HD74LS05P HD14051 IR3M03A PC910	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter triple 3-input NOR hex inverter with MUX/DEMUX DC-DC converter optoisolator	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15179382 15169516 15169514 15169514 15169537 15169334H0 15159113H0 15199159 15229706S0 15189171	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P TC74HC04P TC74HC04P TC74HC27P HD74LS05P HD14051 IR3M03A PC910 M5218P	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter triple 3-input NOR hex inverter with MUX/DEMUX DC-DC converter optoisolator OP amp	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13
13429168  IC  15179246 15229851 15229865 15229863 15219178 15179844 15179845 15449107 15449108 15449109 15179345 15169516 15169516 15169514 15169537 15169334H0 15159113H0 15199159 15229706S0	C8095-90 MB87136A HG61H15B59F HG61H20R36F PCM54HP TC532000P-7471 TC532000P-7472 M5M27C256-A M5M27C256-B M5M27C128-15 M5M4416P-12 HM6264ALSP-15 TC74HC00P TC74HC02P TC74HC04P TC74HC04P TC74HC27P HD74LS05P HD14051 IR3M03A PC910	CPU LA chip gate array reverb chip D/A converter 2M mask ROM 2M mask ROM EP ROM EP ROM EP ROM D RAM S RAM quad 2-input NAND quad 2-input NOR hex inverter triple 3-input NOR hex inverter with MUX/DEMUX DC-DC converter optoisolator	(WAVE) IC21 (WAVE) IC22 IC27 IC26 (reverb,ROM C) IC13

15129172	DTC114T SPT						
15119113	2SA1015GR						
15129136	2SC2878A			1 50.50			
DIODE							Literatura de la composição de la compos
15019126	1SS-133T-77						Main Board
15019291	1SR35-400						CIT D
15019103	1S2473	T 1710					SW Board
15029189	GL-9PG2	LED					green
COIL							
12449305	330µH	DC-D	Ссо	nvert	er		serens (23), too
12449272	GM-50510152	line	fil	ter			
12399501M1	BL02RN-R62	EMI	filt	er			
XTAL							
12389717	12MHz						CPU
12389774	32.768MHz						LA chip
POTENTIOME	TER						
13279832	RK12K1140 50KB	rota	ry				SELECT/VOLUME
RESISTOR					-		
13919303	RML S8 333J	3.3K	Oy 8				
13799762	0.24Ω	meta		ide			
-3177102	O . 2 Tub	me ca	_ 0A				
CAPACITOR							
13639153S0		elec					
13639154S0	1000µF/16V	elec					
13649103J0	10µF/16V	bi-p			100		
13629141	10µF/16V	Phil	lips	1225	109		
CONNECTOR	, CABLE						
13439126	5045-10A		10P	(CN5,	Main	Board)	
13439119	5045-03A					Board)	
13439333	IL-S-2P-S2T2-EF	?		- 1		Board)	
23430525	52011-0610					Main B	
23410578	341-578 (w/lead					ain Boa	
23410577	341-577 (w/lead			- 1		Main Bo	
23410576	341-576 (w/lead	ls)	10P	(SW B	oard-0	CN5, Ma	in Board)
AC ADAPTOR	R			1			
12449546	ACB-100	100V					
12449547	ACB-120	117V					
12449548	ACB-220	220V					
12449549	ACB-240A	240A	(Au	stra1	ia)		
12449564	ACB-240E	240E	(En	gland	)		
MISCELLANE	OUS						
2215051200	Standoff	3x6m	m				
2235533400	Rubber Foot						
23485167	MIDI Cable	1m					

### **CIRCUIT DESCRIPTIONS**

\*1 \*2 \*3 = (1)(2)(3) (Fig.1, Table 1)

CPU (IC34, Main board) processes MIDI IN data by running the operational program (stored in ICs 26 and 27) and reading sound parameters and other sound related data (stored in ROMs and RAMs: ICs 26—31). Then the CPU directs the LA chip IC23 to generate and output the necessary sound. The LA chip places the sound data on the data bus (D0—D15) connecting to both the Reverb chip IC17 and DAC IC8. The LA chip works in timesharing: It outputs data on the clock SH1 which in combination with SH2 and SH3 makes channel select code. If a data is to be reproduced as a direct sound only, the chip places the data during time slots 2 and 6. If reverb effect is required, then slots 1 and 5.

The data put out during slot 1(5) is not only accommodated by the reverb chip  $1C17^2$ but also routed to DMUX where it is delivered to the correct S/H circuit on the code SH1-SH3, as a direct sound. Its reverb counterparts are placed on the data bus when time slot 7(8) comes.

### 回路解説

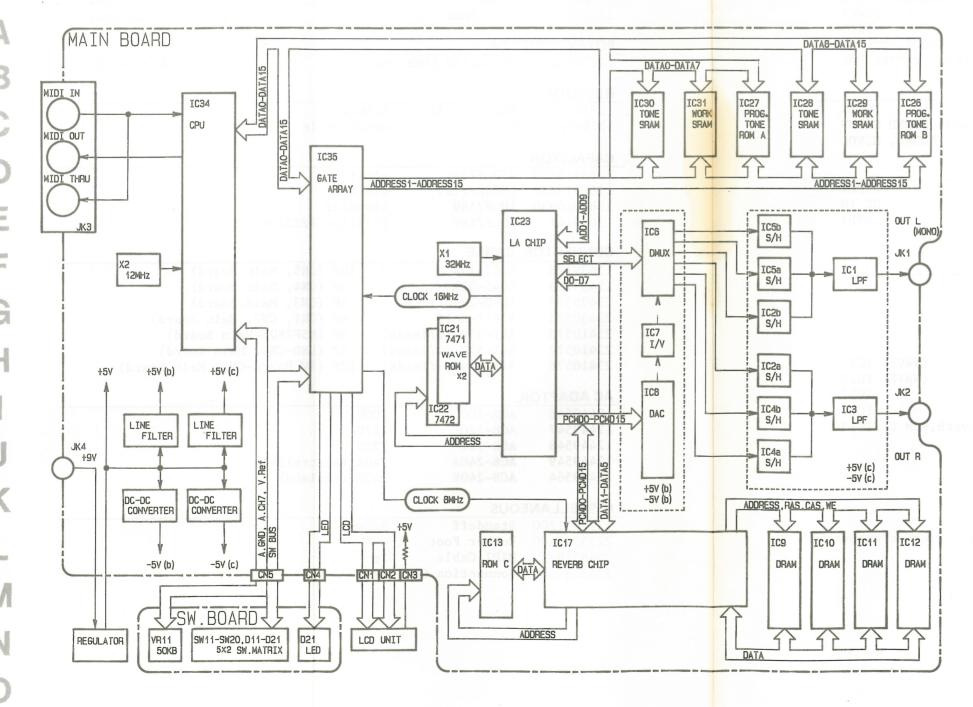
CPU(IC34)はMIDI INデータをオペレーショナルプログラム (IC26, 27) および音のパラメータ・データ (IC26-31) に基づいて処理し、その結果に応じた出力命令 (サウンド出力命令)をLA chip (IC23)へ与える。

LA chipは、サウンドデータを、D0-D15から出力するが、方法は時分割式で、SH1 に同調させている。

Rev を伴なわない音の場合は、タイムスロットの2 と 6 で出力される。Rev を伴う音はスロットの1 と 5 で出力される。後者の場合、データは、Rev chip に取り込まれる $^{*2}$  とともに、DMUXへも送られ、 $^{*1}$  SH1  $^{-1}$  SH3 からなるセレクトコードで目的のS/H 回路へダイレクト音として加えられる。 $^{-1}$   $^{-1}$  Rev chip で得られたリバーブ効果音は、スロット7 と 8 で出力される。 $^{*3}$ 

# 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25

# **BLOCK DIAGRAM**



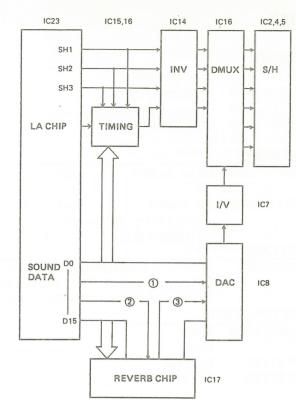


Fig.1

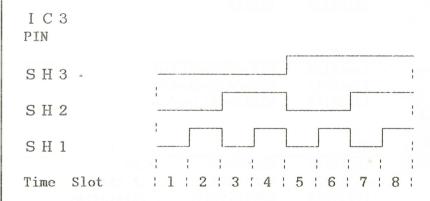
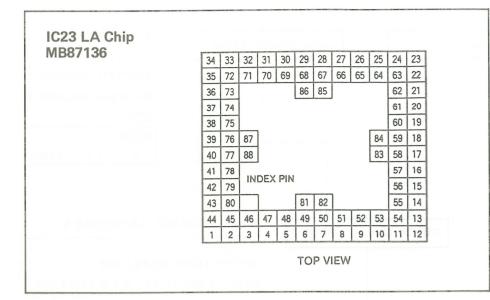


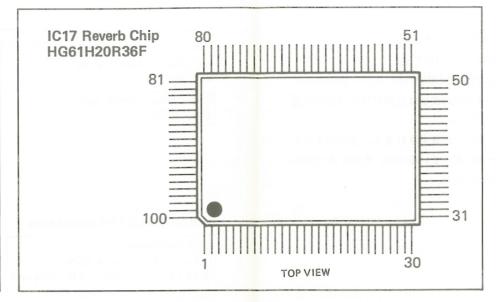
Fig.2

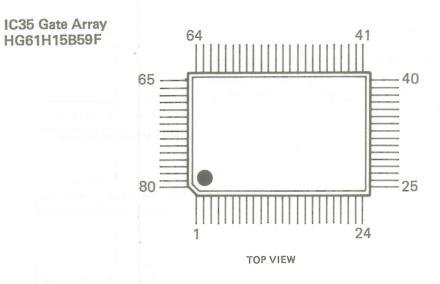
Time Slot	Signa	l Flow	DMUX Output					
5100		61-8513	Pin	Sound				
1	1	2	4	Direct R				
2	1		2	Direct R				
3		9269	)					
4		no so	ouna					
5	1	2	12	Direct L				
6	1		15	Direct L				
7	3		14	Rev R				
8	3		13	Rev L				

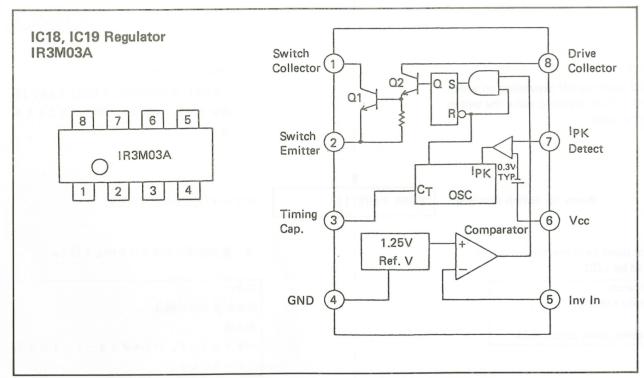
Table 1

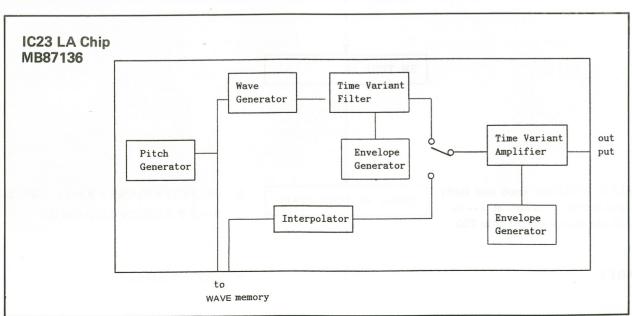
## IC DATA

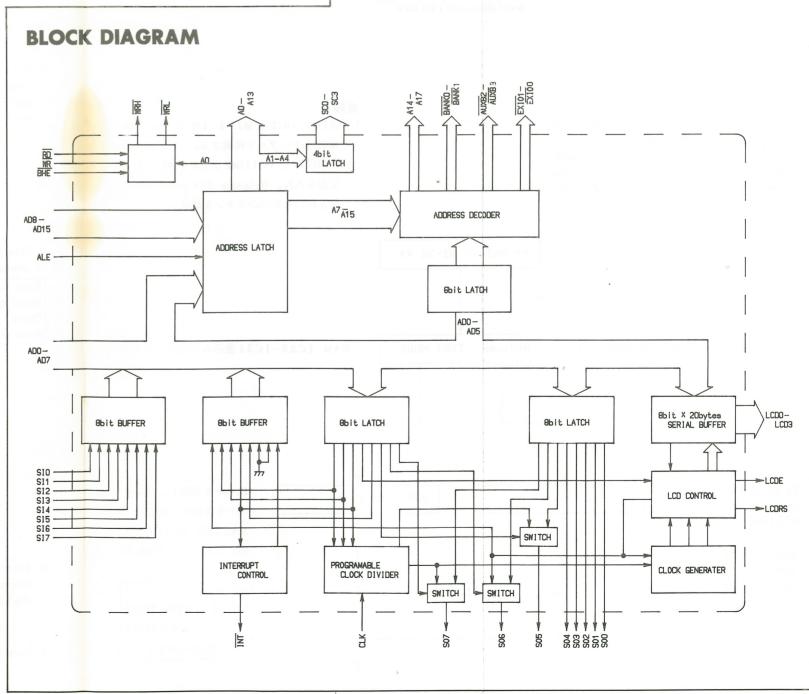


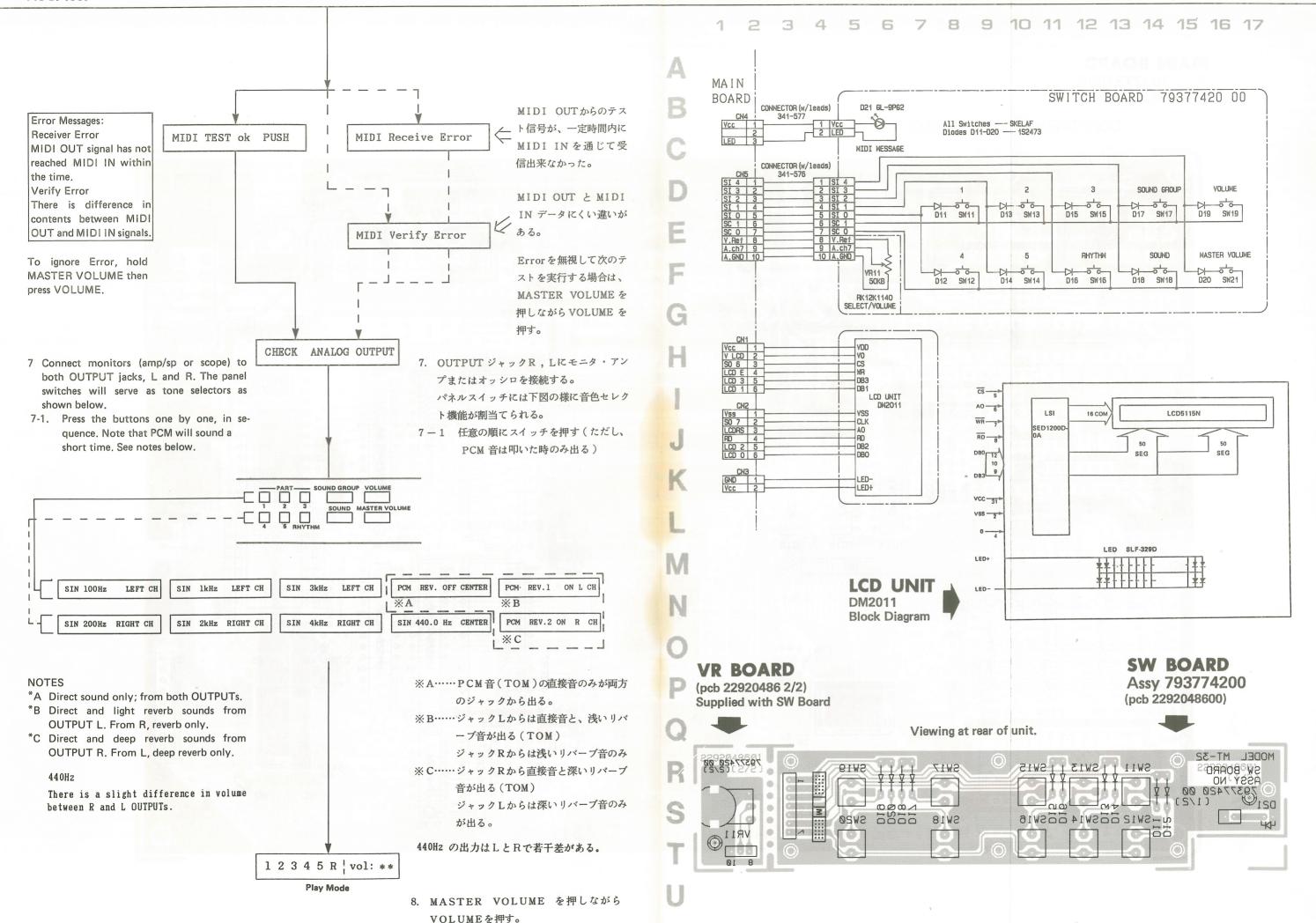








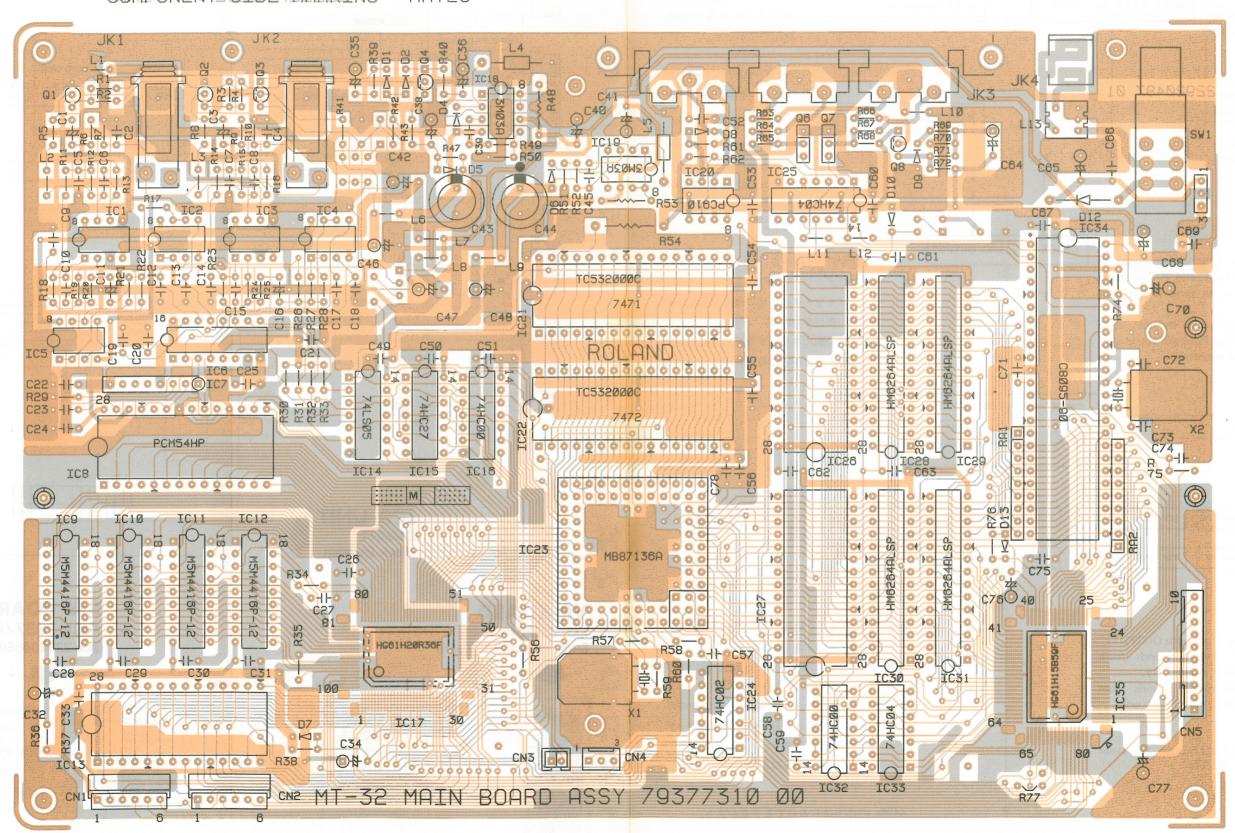




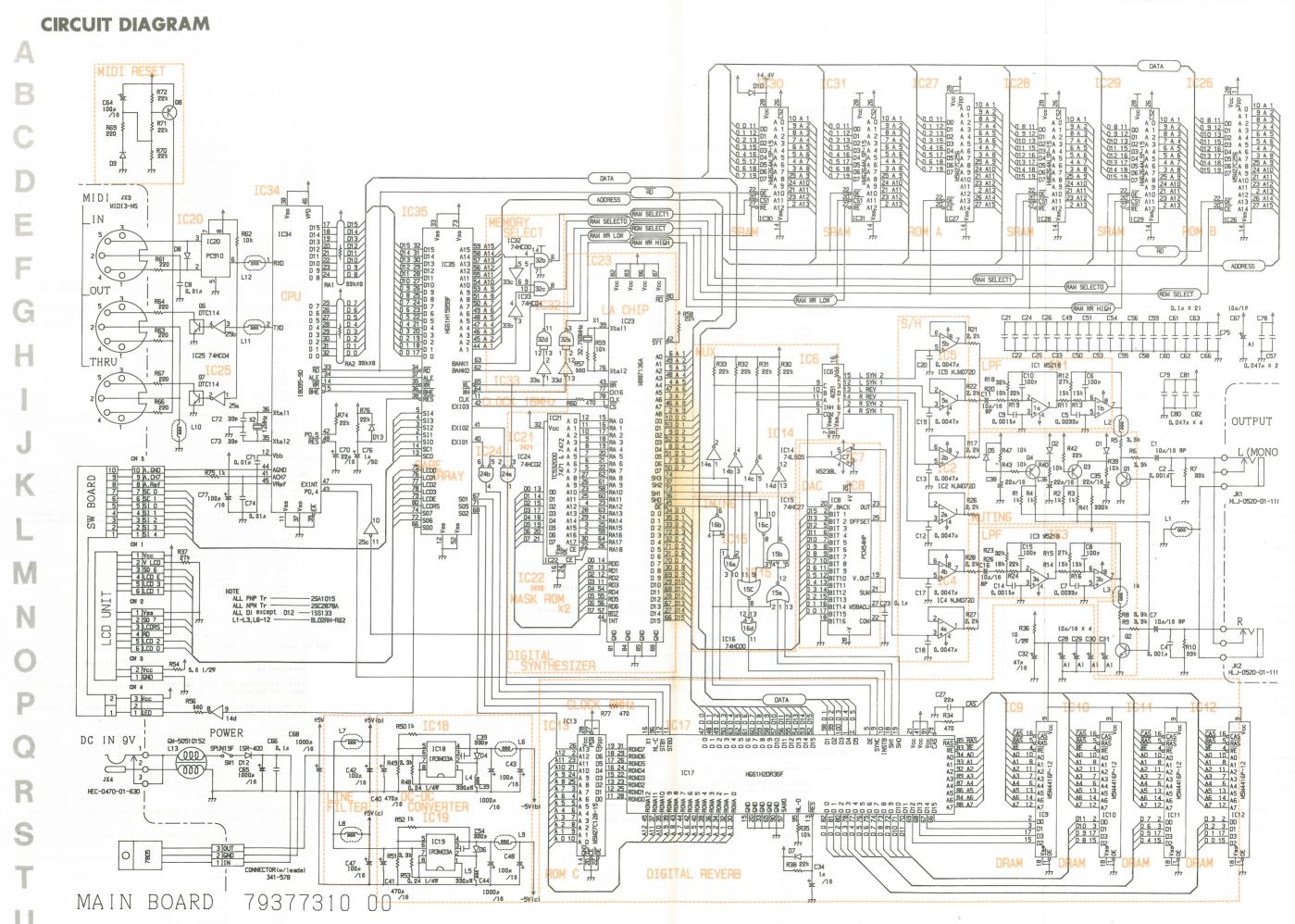
# MAIN BOARD

Assy 7937731000 (pcb 2292048700)

COMPONENT SIDE MARKENG MAY25



1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37



### MIDI IMPLEMENTATION

```
MT-32 MIDI IMPLEMENTATION Version 1.00 Jul. 9 1987
         *** MT-32 MIDI IMPLEMENTATION ***
                                               Version 1.00
Jul. 9 1987
1. TRANSMITTED DATA
Bypassed message
         In Overflow Assign mode, the following MIDI In messages are sent to MIDI Out as
                   · Channel Voice messages except Note On
                    System Exclusive message whose manufacturer ID# is 41H 
Odd Note On(s) left unassigned any voice because all assignable voices are engaged.
Created message
  System exclusive
  Status
         :System Exclusive
         : EOX(End of System Exclusive)
                  See "3.EXCLUSIVE COMMUNICATIONS" for details.
2. RECOGNIZED DATA
Note event
  Note off
         Status
                                               Third
                            Second
           8 n H
                              le le H
                                                vvH
00H
            9 nH
                               kkH
                  kkH : Note number vvH : ignored
                                         OCH - 6CH ( 12 - 108 )
  Note on
         Status
                            Second
                                               Third
                              kkH
            9 n H
                                                vvH
                  vvH : Velocity
Control change
  Continuous controller (14 bits)
         Status
                                               Third
                            Second
1987-Aug- 3 10:20 mt32e.mdi Page 2
           BnH
         Modulation
                            mmH = 01H
         Volume
                             mmH = 07H
                                                       OH - 7FH ( 0 - 127)
         Panpot
                            mmH = OBH
         Expression
  Continuous controller (7 bits)
                                               Third
                                                vvH
           BnH
                              mmH
                                               vvH = 0H - 3FH ( 0 - 63) OFF
40H - 7FH ( 64 - 127) ON
         Hold 1
                            mmH = 40H
        Resets all controllers
mmH = 79h
                                               vvH = 0
m Program change
        Status
                            Second
           CnH
                              ppH
                  ppH : Program number
                                               OH - 7FH (0 - 127)
                  Program Change changes Patch.
Pitch bender
        Status
                            Second
                                              Third
           EnH
                             11H
                  11H : OH - 7FH (0 - 127)
mmH : OH - 7FH (0 - 127)
M Channel mode message
        Status
                           Second
                                              Third
           BnH
                              mmH
                                               оон
                                              7BH (123)
7CH (124)
                   mmH : All Notes Off
                         Omni Off
                         Omni On
Mono On
Poly On
                                              7DH (124)
7EH (124)
                                              7FH (128)
                 Recognized as only All Notes Off.
MT-32 does not change mode, but remains
in mode 3 (Omni off, Poly).
```

```
M Active sensing
           Status
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M System exclusive
  Status
   FOH
            System Fyclusive
            :EOX(End of System Exclusive)
3.
           EXCLUSIVE COMMUNICATIONS
            Model-ID# of MT-32 is 16H.
           MT-32 can receive/send some of the EXCLUSIVE MESSAGEs in the D-50(Roland synthesizer) format.
           Model-ID# of D-50 is 14H.
           Device-ID# is the basic channel# of the each part or Unit# of the MT-32
           Unit# can be changed in "UNIT# SETUP MODE".
Device ID numbers, 0-31, are displayed on the LCD as 1-32,
            respectively.
M One way communication
     Request
                                   RO1 11H
           When the RQ1 received contains a start address listed in
Parameter base address, and address size is 1 or more,
MT-32 sends the corresponding data.
           In Overflow Assign mode, MT-32 does not recognize RQ1, but passes the message to MIDI OUT.
           MT-32 won't transmit RQ1 in the default mode.
                                    Description
                 Byte
                 FOH
                                    Exclusive status
                                   Roland - ID
Device - ID
Model - ID (MT-32(D-50))
Command - ID (RQ1)
                 41H
                 AIH
DEV
16H(14H)
11H
aaH
aaH
aaH
ssH
ssH
                                                                                                  *3-1
                                    Address MSB
Address
Address LSB
Size MSB
                                                                                                  *3-2
                                    Size
Size LSB
                                    Checksum
                                    EOX ( End of Exclusive )
                                   DT1 12H
           When the DT1 contains a start address as defined in RQ1 above, MT-32 stores the data into that memory location.
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           MT-32 sends this message upon receiving RQ1 in the default mode.
           Additional function in Overflow Assign mode:

NT-32 retransmits DT1 while it processes the DT1 data
                       as necessary.
                                    Description
                 Byte
                 FOH
                                    Exclusive status
                                    Roland - ID
Device - ID
Model - ID (MT-32(D-50))
Command - ID (DT1)
Address MSB
                 41H
                 DEV
16H(14H)
12H
                                                                                                  *3-1
                                                                                                  *3-2
                 aaH
                                     Address
                                    Address LSB
                 aaH
ddH
                                    Data
                                                                                                  *3-3
                                    Checksum
EOX ( End of Exclusive )
Handshaking communication
    Want to send data WSD 40H
           Upon receiving WSD, MT-32 sends ACK and waits for DATA SET message.
However, if any part is reproducing sound, MT-32 sends RJC.
            In Overflow Assign mode, MT-32 relays this message to downstream.
            In the default mode, MT-32 won't send this message.
                                    Description
                 Byte
                                   Exclusive status
Roland - ID
Device - ID
Model - ID ( MT-32
Command - ID ( WSD
Address MSB
                 FOH
41H
DEV
16H
40H
aaH
aaH
ssH
ssH
ssH
sum
F7H
                                                                                                   *3-2
                                    Address
Address
Address LSB
Size MSB
Size LSB
Checksum
```

Checksum
EOX ( End of Exclusive )

10

```
Request data
                                     ROD 41H
            When the RQD contains a start address as defined in RQ1 above, MT-32 stores the data into that memory location. However, if any part is reproducing sound, MT-32 sends RJC.
             In Overflow Assign mode, MT-32 relays this message to downstream
            In the default mode, MT-32 won't send this message.
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                                      Exclusive status
                  DEV
16H
41H
aaH
aaH
                                      Roland - ID
Device - ID
Model - ID ( MT-32 )
Command - ID ( RQD )
Address MSB
                                                                                                      *3-2
                                      Address
                                      Address LSB
                                     Address LSB
Size MSB
Size Size Size Checksum
EOX ( End of Exclusive )
                                    DAT 42H
     Data set
            When the DAT contains a start address as defined in RQ1 above, MT-32 stores the data into that memory location. However, if any part is reproducing sound, MT-32 sends RJC.
            In the default mode, MT-32 sends this data upon receipt of RQD
            In Overflow Assign mode, MT-32 relays this message to downstream
            without recognizing it.
                                      Description
                  FOH
41H
DEV
16H
42H
aaH
aaH
                                      Exclusive status
                                     Roland - ID
Device - ID
Model - ID ( MT-32
Command - ID ( DAT
Address MSB
                                                                                                     *3-2
                                      Address LSB
                  Hbb
                                      Data
                                                                                                     *3-3
                                     Checksum
EOX ( End of Exclusive )
                                    ACK 43H
    Acknowledge
            When MT-32 receives this message after sending DAT, it sends the
           when MT-32 receives this message after sending EOD, it ends the current handshaking.
           MT-32 sends ACK when it receives WSD, RQD or DAT in the default mode with no part reproducing sound and with data checksum proves correct.  
                                      Description
                  FOH
41H
DEV
16H
43H
F7H
                                      Exclusive status
                                      Roland - ID
                                     Notand - ID
Device - ID
Model - ID ( MT-32 )
Command - ID ( ACK )
EOX ( End of Exclusive )
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     End of data
                                    EOD 45H
           Upon receiving this message, it sends ACK and ends the current handshaking.
           After finishing the data set(DAT) transmission in the default
            mode. MT-32 sends this message.
           In Overflow Assign mode, MT-32 relays this message to downstream without recognizing the contents.
                                     Description
                  Byte
                  FOH
                                      Exclusive status
                                     Exclusive status
Roland - ID
Device - ID
Model - ID ( MT-32 )
Command - ID ( EOD )
EOX ( End of Exclusive )
                  F7H
     Communication error ERR 4EH
            If checksum doesn't agree ( failure in data reception), MT-32 sends this message.
            When MT-32 receives this message in the default mode, it sends the latest message again.
```

In Overflow Assign mode, MT-32 relays this message to downstream without recognizing it.

Exclusive status
Roland - ID
Device - ID
Model - ID ( MT-32 )
Command - ID ( ERR )
EOX ( End of Exclusive )

Description

Byte

FOH

Rejection

RJC 4FH

If MT-32 receives WSD while it is reproducing sound, it sends RJC.

When MT-32 receives this message, it ends the current handshaking.

In Overflow Assign mode, MT-32 relays this message to downstream without recognizing it.

Byte FOH 41H DEV 16H 4FH F7H Description Exclusive status Exclusive status
Roland - ID
Device - ID
Model - ID (MT-32)
Command - ID (RJC)
EOX (End of Exclusive)

Notes: \*3-1 Both model-IDs are supported. Addresses & parameters are described

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- in section 4 for model-ID 16H(MT-32) and in section 5 for model-ID 14H(D-50,PG-1000).

  \*3-2 Address & Size should be the address where data exist.

  \*3-3 If the data is Partial Reserve Parameter, received data must comprise all the parameters for being recognized.
- Address mapping of parameters

Addresses are shown in Hexa-decimal, while numbers are given in 7 bits.

Address | MSB | | LSB

binary | Oaaa aaaa | Obbb bbbb | Occc cccc
7 bit Hex | AA | BB | CC

The actual address of a parameter in a block is the sum of the start address of each block and one or more offset address. That is, prameters marked by \*4-1 have two offset addresses: one in the table under NOTE \*4-1 and the other in Common parameter table or in Partial parameter table.

Parameter base address

Temporary area ( Accessible on each basic channel )

4					+
1	Sta	art		1	I The Country of the
1		addi	ress	1	Description !
1				-+-	
1	00	00	00	1	Patch Temp Area (part)
1	01	00	00	1	Setup Temp Area (rhythm part)
1	02	00	00.	1	Timbre Temp Area(part) #4-1

Whole part ( Accessible on UNIT# )

÷	+
Start	I service the latest t
l address   Descript	ion !
1 03 00 00   Patch Temp Area	( part 1 ) !
1 03 00 10   Patch Temp Area	( part 2 )
1 : 1	1
1 03 00 60   Patch Temp Area	( part 7 )
1 03 00 70   Patch Temp Area	
1 03 01 00   Patch Temp Area	
1 03 01 10   Setup Temp Area	
l	
1 04 00 00   Timbre Temp Area	( part 1 ) *4-1
1 04 01 76   Timbre Temp Area	
1 04 01 10 1 11 11 11 11 11 11 11 11	
1 04 0b 44   Timbre Temp Area	( part 7 ) *4-1
1 04 0d 3a   Timbre Temp Area	
1 04 00 38 1 11mble 1emp Alea	· part o /
05 00 00   Patch Memory #1	
	a 101
1 05 00 08 1 Patch Memory #2	0.111
1 05 07 70   Patch Memory #12	.7

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05 07	7 78	! Pat	ch Memory	#128	
08 00	0 00	1 Tim	bre Memory	#1	*4-1
08 0	2 00	I Tin	bre Memory	#2	*4-1
	:	1			
08 70	00	1 Tin	bre Memory	#63	*4-1
08 7	E 00	: Tim	bre Memory	#64	*4-1
10 0	0 00	l Sys	tem area		
20 0	0 00	l Dis	play		*4-2
7F x:	x xx	1 A1	parameter	reset	*4-3

Notes \*4-1

:	itri	ictu	re o	Ĩ	"Timbre lemp/Memory"	area	is as i	ollows.	port mail.
1			tart						
		ddr		;	Descripti	o n			To Leaned
1				-+-					
-	00	00	00	1	Common parameter				
1	00	00	OE	1	Partial parameter				31
1	00	00	48	1	Partial parameter	(for	Partial	# 2)	
1	00	01	02	1	Partial parameter	(for	Partial	# 3)	1
-	00	01	3C	1	Partial parameter	(for	Partial	# 4)	1

- \*4-2 The data sent to this address are recognized as the string of letters in ASCII CODE, and displayed on MT-32 LCD. Cannot be called on RQ1 or RQD.
- \*4-3 All parameters will be initialized by sending data to this address.

  Cannot be called on RQ1 or RQD.

Common parameter \*4-4

÷												
Offset address	!		D	es	cription							
1 00H	1	0aaa	aaaa	!	TONE NAME 1		32 (AS			7		
I 09H	1	0aaa	aaaa		TONE NAME 10							
I OAH		0000	aaaa		Structure of		1# 1	8	2	-		12
0BH	1	0000	aaaa	1	Structure of					0	-	12
I OCH	1	0000	aaaa		PARTIAL MUTE		0 -	1	5	11		
I ODH		0000	000a	-	ENV MODE	(N	0 - orma	-	No	su	sta	ain)
Total	s	ize		1	00 00 0EH				_			

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Partial parameter \*4-4

1	Offset   address	Des	cription	
	+		UZ DIMOU GOLDOD	
1	00 00H I	Oaaa aaaa !	WG PITCH COARSE	0 - 96   (C1,C2, - C9)
i	00 01H I	Oaaa aaaa !	WG PITCH FINE	0 - 100   (-50 - +50)
i	00 02H	0000 aaaa	WG PITCH KEYFOLLOW	0 - 16
- 1				(-1,-1/2,-1/4,0,1
1				1/8,1/4,3/8,1/2,1 5/8,3/4,7/8,1,
i	i	i		5/4,3/2,2,s1,s2)
1	00 03H I	0000 000a 1	WG PITCH BENDER SW	0 - 1
	00 04H I	0000 000a	WG WAVEFORM	(OFF, ON) ! 0 - 1 !
i	00 0411 1	1	WG WAYER ORD	(SQU, SAW)
-	00 05H I	Oaaa aaaa I	WG PCM WAVE #	0 - 127
1	00 06H I	Oaaa aaaa	WG PULSE WIDTH	(1 - 128) I 0 - 100 I
1	00 06H 1			0 - 14
1	1	1		(-7 - +7)
		0000 asaa I	P-ENV DEPTH	0 - 10
	00 08H I		P-ENV VELO SENS	0 - 100
	00 OAH		P-ENV TIME KEYF	0 - 4
	00 OBH 1		P-ENV TIME 1	0 - 100
	1 00 OCH 1		P-ENV TIME 2 P-ENV TIME 3	0 - 100 ! 0 - 100 !
	00 ODH 1		P-ENV TIME 4	0 - 100
	00 OFH I		P-ENV LEVEL O	0 - 100
			D Chir I Pupi	(-50 - +50) !
	00 10H I	Oaaa aaaa I	P-ENV LEVEL 1	0 - 100
	00 11H	Oaaa aaaa I	P-ENV LEVEL 2	0 - 100
				(-50 - +50)
	00 12H	Oaaa aaaa I	P-ENV SUSTAIN LEVEL	(-50 - +50)
	00 13H	Oaaa aaaa I	END LEVEL	0 - 100
				(-50 - +50)
	00 14H	Oaaa aaaa I	P-LFO RATE	0 - 100
	00 15H			0 - 100
	00 16H	Oaaa aaaa !	P-LFO MOD SENS	0 - 100
	00 17H	Oaaa aaaa I	TVF CUTOFF FREQ	0 - 100
	00 18H		TVF RESONANCE	0 - 30
	00 19H	0000 aaaa !	TVF KEYFOLLOW	0 - 14
				(-1,-1/2,-1/4,0,1 1/8,1/4,3/8,1/2,1
	08-7	ı		5/8,3/4,7/8,1,
	!		THE BLAC BOLLT (DID	5/4,3/2,2)
	00 1AH	Oaaa aaaa !	TVF BIAS POINT/DIR	0 - 127   - (7C >1A - >7C):
	00 1BH	0000 aaaa l		0 - 14
				(-7 - +7)
	00 1CH	Oaaa aaaa I	TVF ENV DEPTH	0 - 100
	oo ich	vaaa aaaa I	III DEL DEL III	

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1	00	1 DH		0aaa	aaaa	1	TVF	ENV	VELO SE	INS	0	_	100	1
1	0.0	1 EH	1	0000	Caaa	1	TVF	ENV	DEPTH B	EYF	0	-	4	- 1
1		1FH		0000	Oaaa	1	TVF	ENV	TIME KE	YF	0	-	4	
i	0.0	2 O H	1	Oaaa	aaaa	1	TVF	ENV	TIME 1		0	-	100	1
i	0.0	21H	1	0aaa	aaaa				TIME 2					1
1		22H		Oaaa	aaaa	1	TVF	ENV	TIME 3		0	-	100	1
i		23H		Oaaa	aaaa	ı	TVF	ENV	TIME 3		0	-	100	1
1	0.0	2 4 H	1						TIME 5					1
i	00	25H	1						LEVEL 1					1
1	0.0	26H	1						LEVEL 2					
i		27H							LEVEL 3					1
1	00	28H	1	Oaaa	aaaa	1	TVF	ENV	SUSTAIN	LEVE	EL	0	- 100	1
i			-+-											
1	00	29H		0aaa	aaaa	;	TVA	LEVE	EL		0	-	100	-1
1	00	2AH	1	0aaa	aaaa	1	TVA	VELO	SENS		0	-	100	1
1	00	2BH	1	0aaa	aaaa	1	TVA	BIAS	POINT	1	0	-	127	1
1			1			1				(<1A	-	<7	7C >1A -	>7C)1
1	00	2CH	1	0000	aaaa	1	TVA	BIAS	LEVEL	1	0	-	12	1
1			1			ş					(-	12	2 - 0)	1
1	00	2 DH	1	0aaa	aaaa	1	TVA		POINT					1
1			1			1				(<1A	-	<7	7C >1A -	>7C)!
1	00	2 EH	1	0000	aaaa	1	TVA	BIAS	LEVEL				12	1
1			1			1					(-	12	2 - 0)	1

00 2FH | 0000 0aaa | TVA ENV TIME KEYF 0 - 4
00 30H | 0000 0aaa | TVA ENV TIME V\_FOLLOW 0 - 4
00 31H | 0aaa aaaa | TVA ENV TIME 1 0 - 100
00 32H | 0aaa aaaa | TVA ENV TIME 2 0 - 100
00 33H | 0aaa aaaa | TVA ENV TIME 3 0 - 100
00 34H | 0aaa aaaa | TVA ENV TIME 4 0 - 100
00 35H | 0aaa aaaa | TVA ENV TIME 5 0 - 100
00 35H | 0aaa aaaa | TVA ENV LEVEL 1 0 - 100
00 37H | 0aaa aaaa | TVA ENV LEVEL 2 0 - 100
00 37H | 0aaa aaaa | TVA ENV LEVEL 3 0 - 100
00 38H | 0aaa aaaa | TVA ENV LEVEL 3 0 - 100
00 39H | 0aaa aaaa | TVA ENV LEVEL 3 0 - 100 Total size 1 00 00 3AH

M System area

	Offse	ress			D	es	cription						
-	00	00H	!	0aaa	aaaa	!	MASTER	TUNE	0(432.	- 12 1Hz	-	7.	6Hz
-	00	01H	1	0000	00aa	1	REVERB	MODE	(Roor	- 3 n, Ha		Р	late
1	00	02H	1	0000	0aaa	1	REVERB	TIME	0	- 7	8)		
	00	03H	!	0000	0aaa	1	REVERB	LEVEL	0	- 7			
i	00	04H	1	00aa	aaaa	1	PARTIAL	RESERVE	(Part	1)	0	-	32
1	00	05H	1	00aa	aaaa	1	PARTIAL	RESERVE	(Part	2)	0	-	32
	00	06H	1	00aa	aaaa	1	PARTIAL	RESERVE	(Part	3)	0	-	32
1	00	07H	1	00aa	aaaa	1	PARTIAL	RESERVE	(Part	4)	0	-	32
	00	08H	1	00aa	aaaa	1	PARTIAL	RESERVE	(Part	5)	0	-	32
1	0.0	09H	1	00aa	aaaa	1	PARTIAL	RESERVE	(Part	6)	0	-	32

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1		OAH							RESERVE							1
1									RESERVE							ł
1	00	0CH	1	00aa	aaaa	1	PART	IAL	RESERVE	(Par	rt F	()	0	- :	32	1
1-			+			+-										١
1	00	ODH	Ī	000a	aaaa	1	MIDI	CHA	NNEL (Pa	rt 1	)					
1			1			1									OFF)	
1	00	0EH	!	000a	aaaa	!	MIDI	CHA	NNEL (Pa	rt 2	)				16	
1			1			1									OFF)	
1	00	OFH	1	000a	aaaa		MIDI	CHA	NNEL (Pa	rt 3	)				16	
			ł												OFF)	
ī	00	10H	!	000a	aaaa		MIDI	CHA	NNEL (Pa	rt 4.	)				16	
			1												OFF)	
1	. 00	11H		000a	aaaa		MIDI	CHA	NNEL (Pa	rt b	)				16	
1				000					NINET (D	- 1 0					OFF)	
I	00	12H	!	oooa	aaaa	- !	MIDI	CHA	NNEL (Pa	rt o.	,				16	
	00	13H		0000			MIDI	CUA	NNEL (Pa	-4 7					OFF) 16	
	00	1311	i	uuua	aaaa	- 1	MIDI	CHA	MART (La	rt /	,				OFF)	
	00	14H		0000		- :	MIDI	CHA	NNEL (Pa	-4 0					16	
	00	1411		UUUa	aaaa		MIDI	CHA	ININEL (Fa	It o.	,				OFF)	
	00	15H	i	0000		- 1	MIDI	CHA	NNEL (Pa	- D						
	00	1011	:	uuua	aaaa	- 1	MIDI	CHA	MAINET (LS	It n	,				OFF)	
												(1	- I	0,	JFF /	1
!-		1611		^			MACTI	2D 1	OLUME		^	100				1
ı	00	IDH	1	vaaa					OLUME							.;
!-		Total					00 00									
		lotai	5	126			00 00	J 11	п							

Rhythm part setup

ddi	ess	!		D	es	cription	n	
00	00H	1	0aaa	aaaa	1	TIMBRE		0 - 94 (M1-M64,R1-R30,OF
00	01H	1	0aaa	aaaa	1	OUTPUT	LEVEL	0 - 100
00	02H	1	0000	aaaa	1	PANPOT		0 - 14 (R - L)
00	03H	!	0000	000a	1	REVERB	SWITCH	0 - 1 (OFF, ON)

Patch temp

Offset addr		!		D	es	cription	
00	оон	1	0000	00aa	1	TIMBRE GROUP	0 - 3 (GROUP A, GROUP B, MEMORY, RHYTHM)
00	01H	1	00aa	aaaa	1	TIMBRE NUMBER	0 - 63
00	02H	1	00aa	aaaa	i	KEY SHIFT	0 - 48 $(-24 - +24)$
00	03H	1	0aaa	aaaa	1	FINE TUNE	0 - 100 $(-50 - +50)$
00	04H	1	000a	aaaa	1	BENDER RANGE	0 - 24

1	00	05H	1	0000	00aa	1	ASSIGN	MODE	0 - 3
1			1			1			(POLY 1, POLY 2, 1
1			1			1			POLY 3, POLY 4)
1	00	06H	1	0000	000a	1	REVERB	SWITCH	0 - 1
1			1			1			(OFF, ON)
1	00	07H	1	0xxx	xxxx	1	dummy		1
1	00	08H	1	Oaaa	aaaa	1	OUTPUT	LEVEL	0 - 100
1	00	09H	1	0000	aaaa	1	PANPOT		0 - 14
1			1			1			(R - L) !
1	00	OAH	1	0xxx	XXXX	1	dummy		1
1		:	ı			1			1
1	00	OFH	1	0xxx	XXXX	1			
!	;	Total				-+-	00 00	104	
		lota	. 8	126			00 00 .	1011	

#### Patch memory

Offset   address	D	escription	
00 00H I	0000 00aa	! TIMBRE GROUP	0 - 3   (GROUP A, GROUP B, MEMORY, RHYTHM)
00 01H I	00aa aaaa	I TIMBRE NUMBER	0 - 63
00 02H I	00aa aaaa	I KEY SHIFT	0 - 48
1		1	(-24 - +24)
00 03H I	Oaaa aaaa	FINE TUNE	0 - 100 (-50 - +50)
00 04H 1	000a aaaa	BENDER RANGE	0 - 24
00 05H I	0000 00aa	ASSIGN MODE	0 - 3 (POLY 1,POLY 2, POLY 3,POLY 4)
00 06H I	0000 000a	REVERB SWITCH	0 - 1 (OFF, ON)
00 07H I	0xxx xxxx	l dummy	
Total s	ize	1 00 00 08H	

#### DISPLAY

Offset addre	ess	1		D	es	cription		
(	он:	1	0aaa	aaaa	!	DISPLAYED	LETTER	32 - 127 (ASCII)
1	3H	i	0aaa	aaaa	İ	DISPLAYED	LETTER	
To	tal	s	ize		1	00 00 14H		

#### Note:

\*4-4

This parameter can be modified from D-50(PG-1000) and results in accessing the address "02-00-00(Timbre Temp Area(part))"

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of MT-32

ADDRESS MAPPING OF PARAMETERS (compatible with D-50(PG-1000))

#### Parameter base address

Start   address		Descrip	tion						 
00-00-00	1	Partial	3	(	0	-	53	)	i
1 00-00-40	1	Partial	4	(	64	-	117	)	1
1 00-01-0A	1	Upper Co	ommon	(	138	-	175	)	1
1 00-01-40	1	Partial	1	(	192	-	245	)	- 1
1 00-02-00	1	 Partial	2	(	256	-	309	)	1
1 00-02-4A	1	Lower C	ommon	(	330	-	367	)	1
! 00-02-4A		 Lower C	ommon	(	330		367	)	 

#### Partial parameters

Offset	ess	i			Des	cription
00	оон		Oaaa	aaaa		WG PITCH COARSE 0 - 72 (C1,C2, - C7)
00	01H	1	Oaaa	aaaa	1	WG PITCH FINE 0 - 100 (-50 - +50)
00	02H		0000	aaaa	1	WG PITCH KEYFOLLOW 0 - 16 (-1,-1/2,-1/4,0 1/8,1/4,3/8,1/2 5/8,3/4,7/8,1, 5/4,3/2,2,s1,s2
00	03H	i	0xxx	xxxx	i	dummy
0.0	04H	1	0xxx	XXXX	i	dummy
	05H		0000			WG PITCH BENDER SW 0 - 1 (OFF, ON)
00	06H	1	0000	000a	1	WG WAVEFORM 0 - 1 (SQU, SAW)
00	07H	1	0aaa	aaaa	1	WG PCM WAVE # 0 - 99 (1 - 100)
00	08H	1	0aaa	aaaa	1	WG PULSE WIDTH 0 - 100
00	09H	1	0000	aaaa	1	WG PW VELO SENS 0 - 14
		1			1	(-7 - +7)
		+			+	
	OAH		0xxx		-	dummy
	OBH		0xxx			dummy
00	OCH	1	0xxx	XXXX	1	dummy

1	00	ODH	1	0aaa	aaaa	1	TVF	CUTO	FF FREQ	0 - 100	1
1	00	0EH	1	000a	aaaa	1	TVF	RESO	NANCE	0 - 30	1
1	00	OFH	1	0000	aaaa	1	TVF	KEYF	OLLOW	0 - 14	
1			1			1				(-1, -1/2, -1/4)	,0,1
1			1			1				1/8,1/4,3/8,1	/2,1
1			1			1				5/8,3/4,7/8,1	, 1
1			1			1				5/4,3/2,2)	1
ŧ	00	10H	1	0aaa	aaaa	1	TVF	BIAS	POINT/DIR		1
1			:			1			(<1A	- (7C >1A - >	7C)
1	00	11H	1	0000	aaaa	1	TVF	BIAS	LEVEL	0 - 14	1

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00	12H I	Oaaa aaa	a I TVF	ENV DEPTH 0 - 100
00	13H I	Oaaa aaa	a I TV	ENV VELO SENS 0 - 100
00	14H I	0000 0aa	a I TV	ENV DEPTH KEYF 0 - 4
00	15H	0000 0as	a ! TV	ENV TIME KEYF 0 - 4
00	16H I	Oaaa aaa	a ! TVF	ENV TIME 1 0 - 100
00	17H I	Oaaa aaa	a I TV	ENV TIME 2 0 - 100 ENV TIME 3 0 - 100
00	18H !	Oaaa aaa	a I TVF	ENV TIME 3 0 - 100
00	19H I	Oaaa aaa	a I TVI	ENV TIME 4 0 - 100
00	1AH I	Oaaa aaa		ENV TIME 5 0 - 100
00	1BH I			ENV LEVEL 1 0 - 100
00	1CH			ENV LEVEL 2 0 - 100
00	1DH I			ENV LEVEL 3 0 - 100
00	1EH I			ENV SUSTAIN LEVEL 0 - 100
00	1FH	0xxx xxx	x   du	nmy
	: 1		1	
00	22H !	0xxx xxx	x I du	nmy
	23H I	Oaaa aaa	a I TV	LEVEL 0 - 100
00	24H	Oaaa aaa	a ! TV	VELO SENS 0 - 100
00	25H I	Oaaa aaa	a I TV	BIAS POINT 1 0 - 127
	1		1	((1A - (7C )1A - )
00	26H I	0000 aa	a I TV	BIAS LEVEL 1 0 - 12
				(-12 - 0)
00	27H I	Oaaa aa		ENV TIME 1 0 - 100
00	28H I	Oaaa aa		ENV TIME 2 0 - 100
00	29H I	Oaaa aaa	a I TV	ENV TIME 3 0 - 100
00	2AH I	Oaaa aaa	a ! TV	ENV TIME 4 0 - 100
0.0	2BH I	Oaaa aaa		ENV TIME 5 0 - 100
00	2 CH 1			ENV LEVEL 1 0 - 100
00	2DH I			ENV LEVEL 2 0 - 100
00	2 EH			ENV LEVEL 3 0 - 100
	2FH I			ENV SUSTAIN LEVEL 0 - 100
	30H I		ex I du	
0.0	31H I			ENV TIME V_FOLLOW 0 - 4
	32H I			ENV TIME KEYF 0 - 4
	33H I		x I du	
00				mv
00 00 00	34H I 35H I	0xxx xxx		

### Lower common parameter

Offse	t	1																	
add	ress	1		I	Des	cri	pti	on											
00	оон		0000	aaaa	- 1	St	ruc	ture	of	Pa	rt	ia	1#	1	8	2	0	-	12
		1			1												(1	-	13)
00	01H	1	0aaa	aaaa	- 1	P-	ENV	VELO	S	ENS	(P	ar	t i	al	#1	)	0	-	100
00	02H	1	0000	0aaa	1	P-	ENV	TIME	EK	EYF	(P	ar	t i	al	#1	)	0	-	4
00	03H	:	0aaa	aaaa	1	P-	ENV	TIME	3 1	(Pa	rt	ia	1#	1)			0	-	100
00	04H	1	0aaa	aaaa	1	P-	ENV	TIME	3	(Pa	rt	ia	1#	1)			0	-	100
00	05H	1	Oaaa	aaaa	1	P-	ENV	TIME	3	(Pa	rt	ia	1#	1)			0	-	100
00	06H	1	0aaa	aaaa	1	P-	ENV	TIME	3 4	(Pa	rt	ia	1#	1)			0	-	100
00	07H	1	Oaaa	aaaa	1	P-	ENV	LEVI	EL.	OCP	ar	ti	al	#1	)		0	-	100

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		1			1	(-50	- +50)
00	08H	i	Oaaa	aaaa	-	P-ENV LEVEL 1(Partial#1)	0 - 100
201		1			1	(-50	- +50)
00	09H	1	0aaa	aaaa	1	P-ENV LEVEL 2(Partial#1)	0 - 100
		1			1	(-50	- +50)
00	OAH	1	0aaa	aaaa	1	P-ENV SUS LEVEL (Partial#1)	0 - 100
		1			1	(-50	
00	OBH	1	0aaa	aaaa	1	END LEVEL(Partial#1)	0 - 100
		1			!	(-50	- +50)
00	OCH	1	0xxx	xxxx	1	dunmy	
	ODH		Oaaa	aaaa	1	P-LFO MOD SENS(Partial#1)	0 - 100
00	OEH	1	Oaaa	aaaa	1	P-LFO MOD SENS(Partial#2)	0 - 100
00	OFH	1	0xxx	XXXX	1		
ÒO	1 OH	1	Oaaa	aaaa	1	P-LFO RATE(Partial#1)	0 - 100
00	11H	1	Oaaa	aaaa	1	P-LFO DEPTH(Partial#1)	0 - 100
00	12H	1	0xxx	XXXX	1	dunmy	
00	13H	1	0xxx	XXXX	1		
00	14H	1	0aaa	aaaa		P-LFO RATE(Partial#2)	
00	15H	1	0aaa	aaaa	1	P-LFO DEPTH(Partial#2)	0 - 100
00	16H	1	0xxx	XXXX	:	dummy	
	:	1			1		
00	23H	1		XXXX	1	dummy	
0.0	24H	1	0000	00aa	1	PARTIAL MUTE(Partial# 182)	
		!			1		(00 - 11)
00	25H	1	0xxx	XXXX	1	dummy	
	Total	-+-	ize		1	00 00 26H	

Upper common parameter

÷							
1 (	Offset		1				
1	addr	ess	1		D	es	cription !
1			+-				
1	00	00H	1	0000	aaaa	1	Structure of Partial# 1 & 2 0 - 12 !
1			1			1	(1 - 13)!
1		01H	1		aaaa	ī	P-ENV VELO SENS(Partial#3) 0 - 1001
!			i		0aaa		P-ENV TIME KEYF(Partial#3) 0 - 4 !
1			1		aaaa	1	P-ENV TIME 1(Partial#3) 0 - 1001
1		04H	1		aaaa	1	P-ENV TIME 2(Partial#3) 0 - 100!
1		0011	1		aaaa	1	P-ENV TIME 3(Partial#3) 0 - 1001
1		06H	1		aaaa	1	P-ENV TIME 4(Partial#3) 0 - 100!
1	00	07H	1	0aaa	aaaa	1	P-ENV LEVEL O(Partial#3) 0 - 1001
1			1			1	(-50 - +50)!
t	00	08H	I	0aaa	aaaa	1	P-ENV LEVEL 1(Partial#3) 0 - 1001
1			I			1	(-50 - +50)
1	00	09H	1	0aaa	aaaa	1	P-ENV LEVEL 2(Partial#3) 0 - 1001
1			1			1	(-50 - +50)!
1	00	OAH	t	0aaa	aaaa	1	P-ENV SUS LEVEL(Partial#3) 0 - 1001
ı			1			i	(-50 - +50)1
1	00	OBH	1	0aaa	aaaa	ł	END LEVEL(Partial#3) 0 - 1001
!			1			1	(-50 - +50)1
!			-+-			-+-	
!		0CH			XXXX		dummy
!		0211	1		aaaa		P-LFO MOD SENS(Partial#3) 0 - 100
1			!		aaaa	1	P-LFO MOD SENS(Partial#4) 0 - 100
1			1		XXXX	1	dummy
1		10H	1		aaaa	1	P-LFO RATE(Partial#3) 0 - 100
1	00	11H	1	Oaaa	aaaa	1	P-LFO DEPTH(Partial#3) 0 - 100

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1 0	00 13H   00 14H   00 15H   00 16H	Oaaa Oaaa	xxxx aaaa aaaa	1	dummy P-LFO RATE(Partial#4) P-LFO DEPTH(Partial#4)	0 - 100 0 - 100
1 0	0 15H	Oaaa	aaaa	1		
					P-LFO DEPTH(Partial#4)	0 - 100
1 0	0 16H	0xxx	~~~			
1			~~~~	1	dummy	
	:			1		
1 0	0 23H	0xxx	XXXX	1	dummy	
1 0	0 24H	0000	00aa	1	PARTIAL MUTE(Partial# 384)	0 - 3
1	1			1		(00 - 11)
1 0	0 25H	0xxx	XXXX	1	dummy	

### MIDI Implementation Chart

[ Multi Timbre Soud Module ] Date : Jun.19 1987 Model MT-32 MIDI Implementation Chart Version : 1.00

+			
Function	Transmitted	Recognized	Remarks
Basit Default Channel Changed		1 - 16 1 - 16	Memorized
Default : Mode Messages	******	Mode 3	
	* 0 - 127		
		o v= 1 - 127	
	*		
:Pitch Bender		o 0 - 24 semi	
: 1: 7: Control 10: 11:	* * * *	0	Modulation Part Volume Panpot Expression
Change   12	*	ж	
64:		o X	Holdl
120:			ResetSall con- trollers
Prog		o 0 - 127 0 - 127	
	0 #	0	
System: Song Pos: Song Sel: Common: Tune	x x	x x x	
System   Clock   Real Time   Commands	x	x x	
:	x x	x o (123-127)	
Notes	* in OVERFLOW MC	DE, received mess	age goes thru MIDI OUT
Mode 1 : OMNI ON, PO Mode 3 : OMNI OFF, PO		INI ON, MONO	o : Yes x : No